

## TRANSMITTAL OF INFORMATION DISCLOSURE STATEMENT

<b>In re application of:</b>	Thomas A. Petersen Sanjay Vishin
<b>Serial No.:</b>	NA
<b>Filed:</b>	Herewith
<b>Docket:</b>	MIPS.0187.02-00US
<b>For:</b>	METHOD AND APPARATUS FOR GLOBAL ORDERING TO INSURE LATENCY INDEPENDENT COHERENCE


Attached hereto is Form PTO-1449 listing documents believed relevant to the subject application. It is respectfully requested that the Examiner review the information disclosed herein in detail, independently evaluate each item carefully in the consideration of the pending claims and return an initialed copy of each form to the undersigned.

This disclosure statement should not be construed as a representation that a search has been made, that no other material information as defined in 37 C.F.R. § 1.56(a) exists, or as an admission that the information cited in the statement is, or is considered to be, material to patentability as defined in 37 CFR § 1.56(b) or is available as a reference under 35 U.S.C. § 102 *et seq.* Applicant reserves the right to swear behind or otherwise disprove the alleged "prior" nature of any art cited should the facts support and the situation warrant such an action.

It is believed that this disclosure complies with the requirements of 37 C.F.R. §§ 1.56, 1.97 and 1.98, and the Manual of Patent Examining Procedures § 609. If for some reason the examiner considers otherwise, it is respectfully requested that the undersigned be called so that any deficiencies can be remedied.

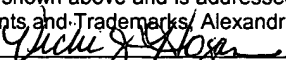
A copy of each document is enclosed. Some of the documents may have markings thereon. No significance is intended to be attached to the markings.

Respectfully submitted,

  
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Date: 2-20-04

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Substitute for form 1449A/PTO  <b>INFORMATION DISCLOSURE STATEMENT BY APPLICANT</b>  (use as many sheets as necessary)				<b>Complete if Known</b>	
				Application Number	NA
				Filing Date	Herewith
				First Named Inventor	Thomas A. Petersen Sanjay Vishin
				Group Art Unit	
				Examiner Name	
Sheet	1	of	1	Attorney Docket Number	MIPS.0187.02-00US

OTHER PRIOR ART-NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No. <sup>1</sup>	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T <sup>2</sup>	
	AA	JOHN KUBIATOWICZ, DAVID CHAIKEN, ANANT AGARWAL; "Closing the Window of Vulnerability in Multiphase Memory Transactions"; Laboratory for Computer Science; Massachusetts Institute of Technology; Cambridge, MA 02139; Appeared in ASPLOS V, October 1992.		
	AB	WILLIAM J. DALLY AND BRIAN TOWLES; "Route Packets, Not Wires: On-Chip Interconnection Networks"; Computer Systems Laboratory; Stanford University, Stanford, CA 94305		
	AC	SARITA V. ADVE, KOUROSH GHARACHORLOO; "WRL Research Report 95/7; "Shared Memory Consistency Models: A Tutorial"; Western Research Laboratory, 250 University Avenue, Palo Alto, CA 94301.		

Examiner Signature		Date Considered	
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\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

<sup>1</sup>Unique citation designation number. <sup>2</sup>Applicant is to place a check mark here if English language Translation is attached.

Burden Hour Statement: This form is estimated to take 2.0 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Assistant Commissioner for Patents, Washington, DC 20231.